Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

Applications

- Power management in half bridge and inverters
- DC-DC Converter
- Load Switch

General Description

The ZX 4953 is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The ZX 4953 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

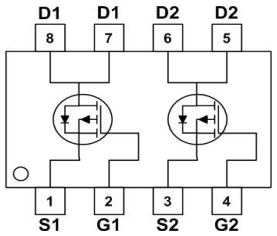
Product Summary

BVDSS	RDSON	ID
-20V	55mΩ	-5 A

SOP-8 Pin Configuration



pin assignment



Schematic diagram

Package Marking and Ordering Information

Product	Package	Marking	Packing	Min Unit Quantity
ZX 4953	SOP8	*	3000PCS/Reel	3000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	±12	V
I_D@T_C=25°C	Continuous Drain Current, $-V_{GS}$ @ -10V[1]	-5.0	A
I_D@T_C=100°C	Continuous Drain Current, $-V_{GS}$ @ -10V[1]	-3.0	A
I_{DM}	Pulsed Drain Current[2]	-20	A
P_D@T_C=25°C	Total Power Dissipation[3]	2.0	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	85	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	36	°C/W

Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D= -250μA	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}= -20V, V_{GS}=0V,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS}= ±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D= -250μA	-0.5	-0.7	-1.0	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note2	V_{GS}= -4.5V, I_D= -3A	-	55	70	mΩ
		V_{GS}= -2.5V, I_D= -2A	-	70	100	
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}= -10V, V_{GS}=0V, f=1.0MHz	-	503	-	pF
C_{oss}	Output Capacitance		-	67	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58	-	pF
Q_g	Total Gate Charge	V_{DS}= -10V, I_D= -2A, V_{GS}= -4.5V	-	4.1	-	nC
Q_{gs}	Gate-Source Charge		-	0.8	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	1.1	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}= -10V, I_D= -3A, R_G=1Ω, V_{GEN}= -4.5V, R_L=1.2Ω	-	11	-	ns
t_r	Turn-on Rise Time		-	52	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	16	-	ns
t_f	Turn-off Fall Time		-	10	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-5	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-12	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S= -3A	-	-	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

P-Channel Typical Characteristics

Figure1: Output Characteristics

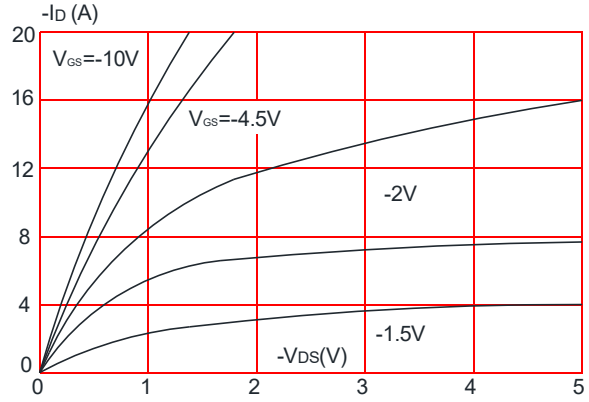


Figure 2: Typical Transfer Characteristics

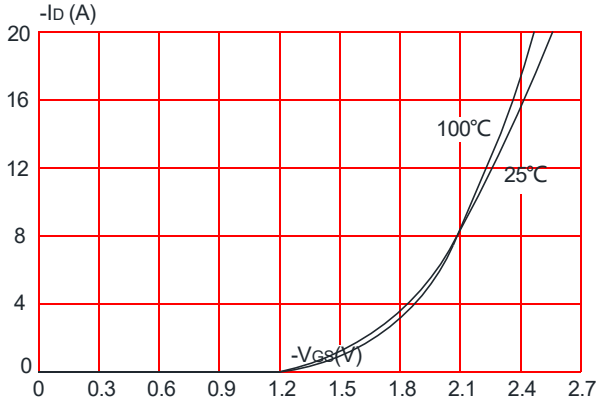


Figure 3: On-resistance vs. Drain Current

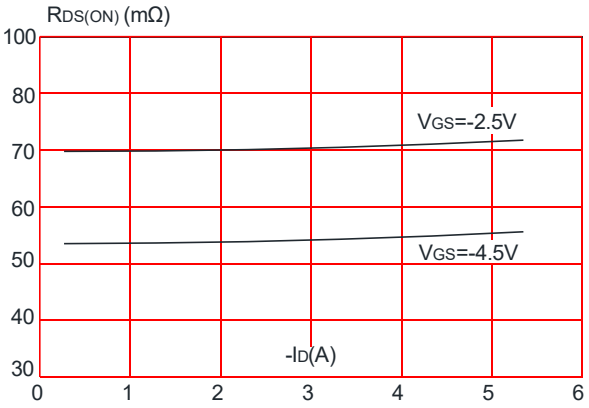


Figure 4: Body Diode Characteristics

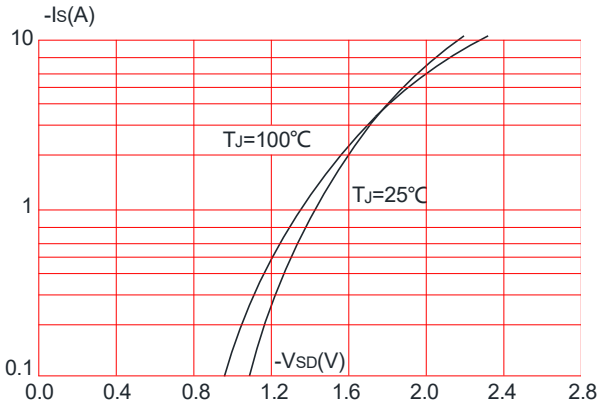


Figure 5: Gate Charge Characteristics

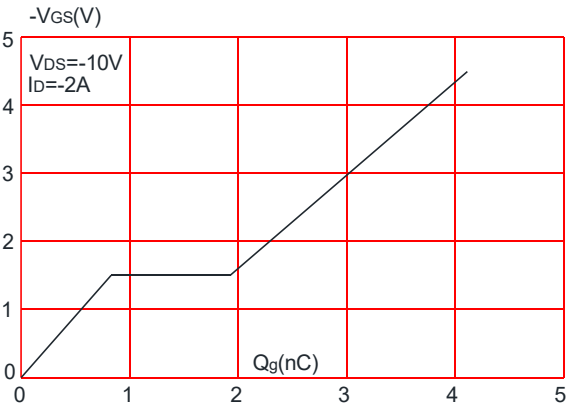
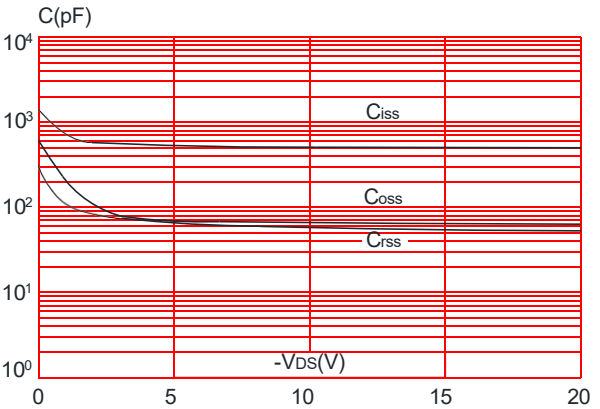
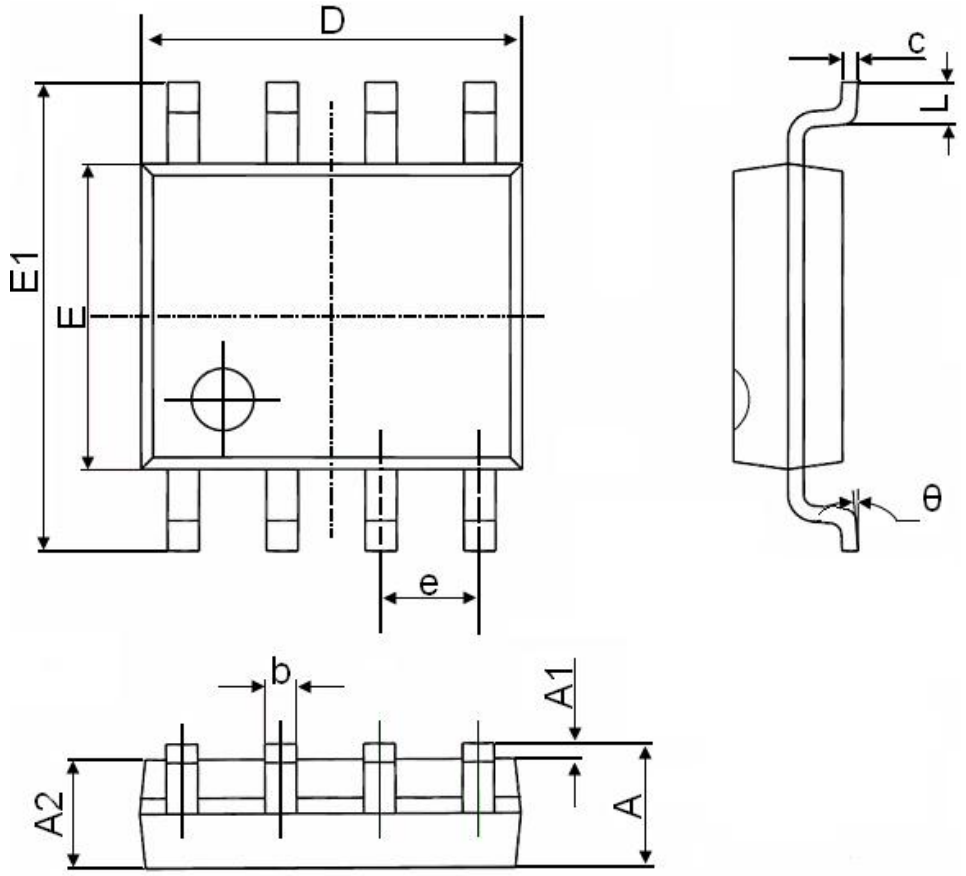


Figure 6: Capacitance Characteristics



SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°