- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

BVDSS	RDSON	ID
-30V	41mΩ	-4.5A

Description

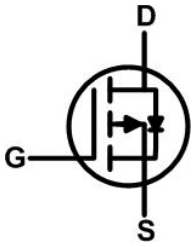
The ZX 3407A is the high cell density trenched P-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

The ZX 3407A meet the RoHS and Green Product requirement with full function reliability approved.

SOT23-3L Pin Configuration



pin assignment



Schematic diagram

Package Marking and Ordering Information

Product	Package	Marking	Packing	Min Unit Quantity
ZX 3407A	SOT23-3L	*	3000PCS/Reel	3000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_A=25℃	Continuous Drain Current	-4.5	A
I_D@T_A=70℃	Continuous Drain Current	-3.6	A
I_{DM}	Pulsed Drain Current[2]	-16	A
P_D@T_A=25℃	Total Power Dissipation[3]	1.5	W
P_D@T_A=70℃	Total Power Dissipation[3]	1.0	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	105	℃/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1] (t ≤10s)	---	---	℃/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}= 0V, I_D= -250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}= -30V, V_{GS}= 0V	-	-	-1	μA
Gate-Source Leakage	I_{GSS}	V_{DS}= 0V, V_{GS}= ±20V	-	-	±100	nA
Gate-Source Threshold voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D= -250μA	-1	-1.5	-2.5	V
Drain-Source on-State Resistance[3]	$R_{DS(on)}$	V_{GS}= -10V, I_D= -4.1A	-	41	60	mΩ
		V_{GS}= -4.5V, I_D= -3A	-	53	85	
Dynamic Characteristics[4]						
Input Capacitance	C_{iss}	V_{GS} = 0V , V_{DS} = -15V, f = 1.0MHz	-	530	-	pF
Output Capacitance	C_{oss}		-	70	-	
Reverse Transfer Capacitance	C_{rss}		-	56	-	
Switching Characteristics[4]						
Total Gate Charge	Q_g	V_{GS} = -10V, V_{DS} = -15V, I_D = -4.1A	-	6.8	-	nC
Gate−Source Charge	Q_{gs}		-	1.0	-	
Gate−Drain Charge	Q_{gd}		-	1.4	-	
Turn-on Delay Time	$t_{d(on)}$	V_{GS}= -10V, V_{DS}= -15V , R_L= 15Ω,R_{GEN}= 2.5Ω	-	14	-	ns
Rise Time	t_r		-	61	-	
Turn-off Delay time	$t_{d(off)}$		-	19	-	
Fall Time	t_f		-	10	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage[3]	V_{SD}	I_S = -4.1A, V_{GS} = 0V	-	-	-1.2	V
Continuous Source Current	I_S		-	-	-4.5	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
2. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

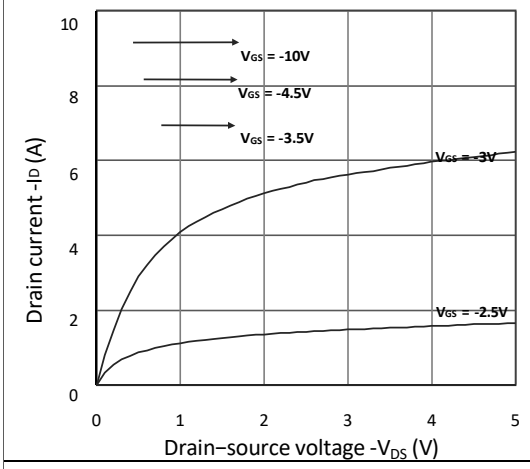


Figure 1. Output Characteristics

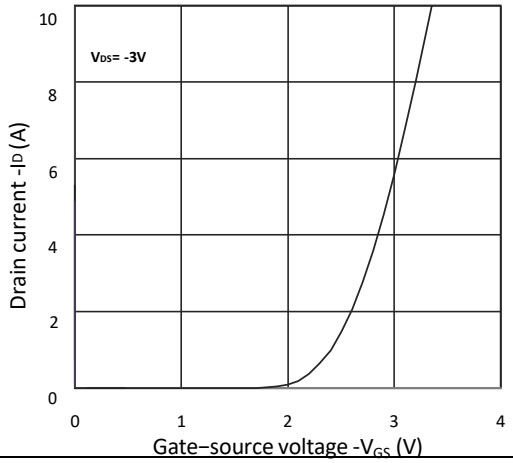


Figure 2. Transfer Characteristics

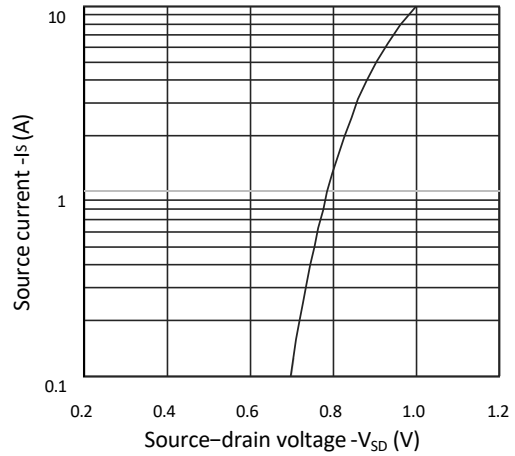


Figure 3. Forward Characteristics of Reverse

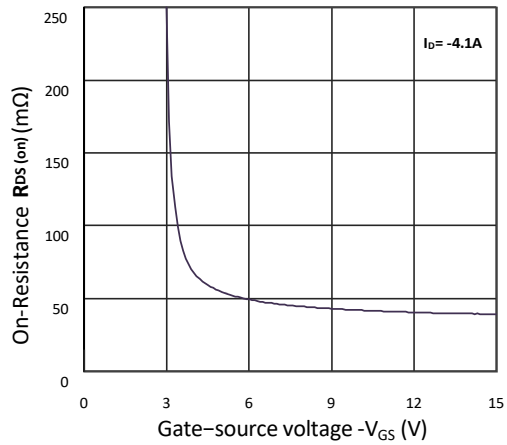


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

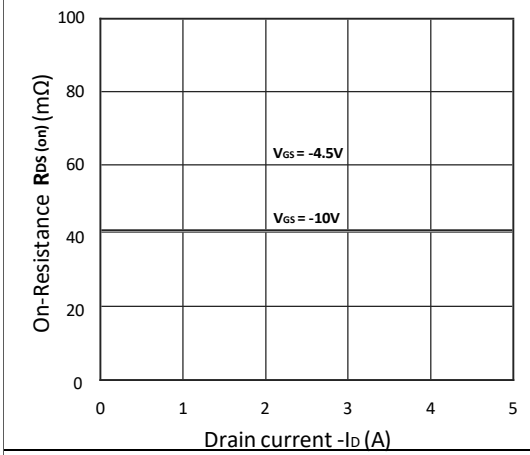


Figure 5. $R_{DS(ON)}$ vs. I_D

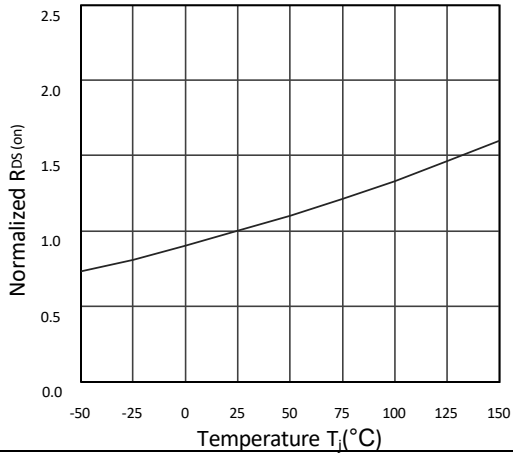


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

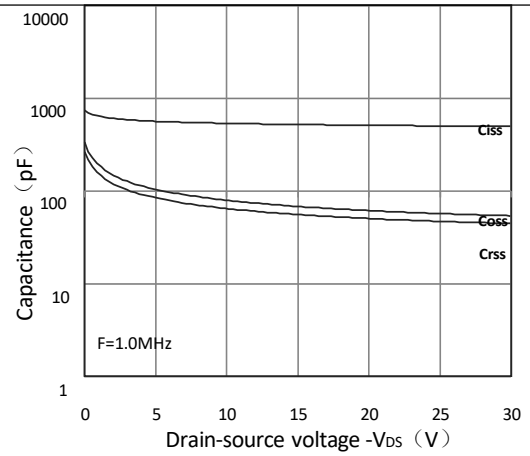


Figure 7. Capacitance Characteristics

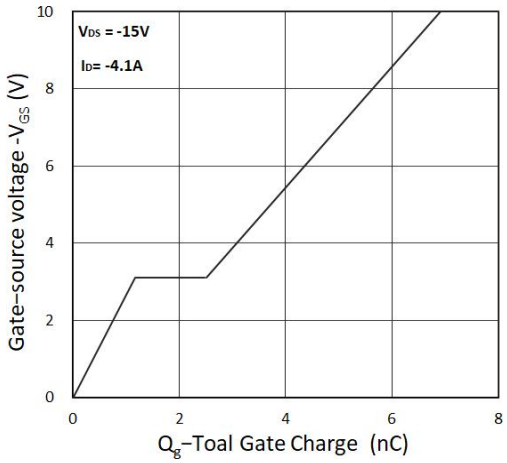
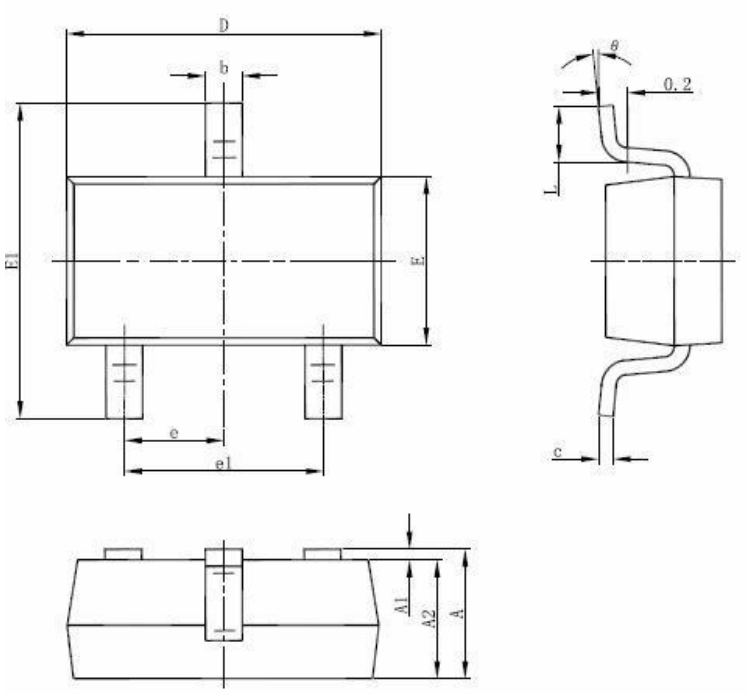


Figure 8. Gate Charge Characteristics

Package Mechanical Data SOT 23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°