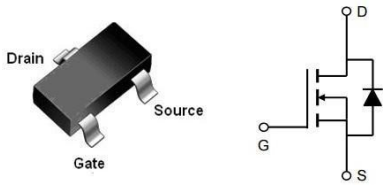
Features

- *Low $R_{DS(on)}$ @V_{GS}=4.5V*
- *3.3V Logic Level Control*
- *N Channel SOT23 Package*
- *Pb−Free, RoHS Compliant*

$V_{(BR)DSS}$	$R_{DS(ON)}$ Typ	I_D Max
20V	28mΩ @ 4.5V	3.6A
	32mΩ @ 3.3V	

Applications

- Load Switch
- *DC/DC Converter*
- *Switching Circuits*
- *LED Driver*



SOT23

Order Information

Product	Package	Marking	Packing	Min Unit Quantity
SI2302	SOT23	A2SHB	3000PCS/Reel	3000PCS

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter		Rating	Unit
Common Ratings (T_A=25°C Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage		±10	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage		20	V
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-50 to 150	°C
Mounted on Large Heat Sink				
I_{DM}	Pulse Drain Current Tested①	T_A=25°C	18	A
I_D	Continuous Drain Current(V_{GS}=4.5V)	T_A=25°C	3.6	A
		T_A=70°C	3.0	
P_D	Maximum Power Dissipation	T_A=25°C	1.2	W
		T_A=70°C	0.9	
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		100	°C/W

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=250μA	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current(T_A=25℃)	V_{DS}=20V, V_{GS}=0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T_A=125℃)	V_{DS}=16V, V_{GS}=0V	--	--	100	uA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±10V, V_{DS}=0V	--	--	±100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	0.4	0.6	1.0	V
$R_{DS(ON)}$	Drain-Source On-State Resistance②	V_{GS}=4.5V, I_D=4A	--	28	35	mΩ
$R_{DS(ON)}$	Drain-Source On-State Resistance②	V_{GS}=3.3V, I_D=2A	--	32	40	mΩ
$R_{DS(ON)}$	Drain-Source On-State Resistance②	V_{GS}=2.5V, I_D=1A	--	36	45	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
C_{iss}	Input Capacitance	V_{DS}=10V, V_{GS}=0V, f=1MHz	--	280	--	pF
C_{oss}	Output Capacitance		--	46	--	pF
C_{rss}	Reverse Transfer Capacitance		--	42	--	pF
Q_g	Total Gate Charge	V_{DS}=10V I_D=3A, V_{GS}=5V	--	4.7	--	nC
Q_{gs}	Gate Source Charge		--	0.6	--	nC
Q_{gd}	Gate Drain Charge		--	1.7	--	nC
Switching Characteristics						
$t_{d(on)}$	Turn on Delay Time	V_{DD}=10V, I_D=4A, R_G=3.3Ω, V_{GS}=4.5V	--	11	--	ns
t_r	Turn on Rise Time		--	35	--	ns
$t_{d(off)}$	Turn Off Delay Time		-	25	--	ns
t_f	Turn Off Fall Time		--	32	--	ns
Source Drain Diode Characteristics						
I_{SD}	Source drain current(Body Diode)	T_A=25℃	--	--	1.8	A
V_{SD}	Forward on voltage②	T_j=25℃, I_{SD}=2A, V_{GS}=0V	--	0.74	1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

② Pulse test ; Pulse width 300μs, duty cycle 2%.

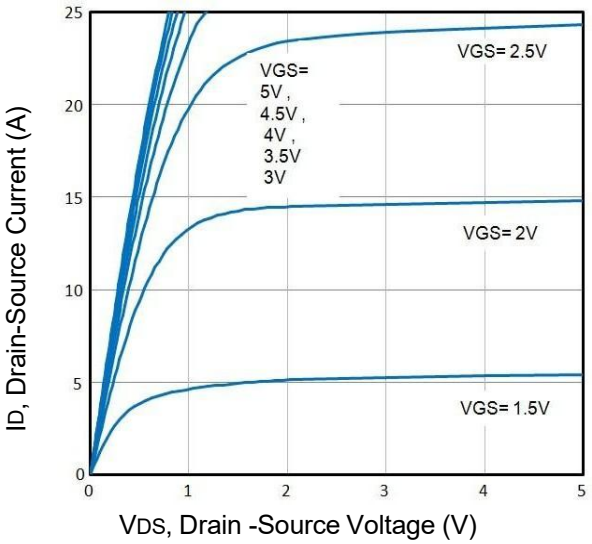


Fig1. Typical Output Characteristics

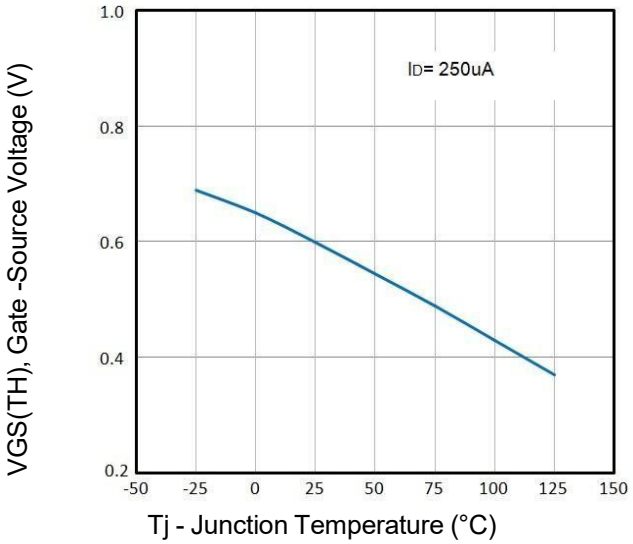


Fig2. Normalized Threshold Voltage Vs. Temperature

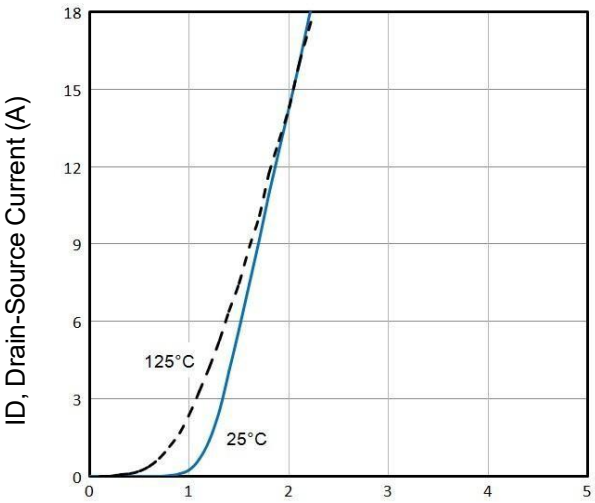


Fig3. Typical Transfer Characteristics

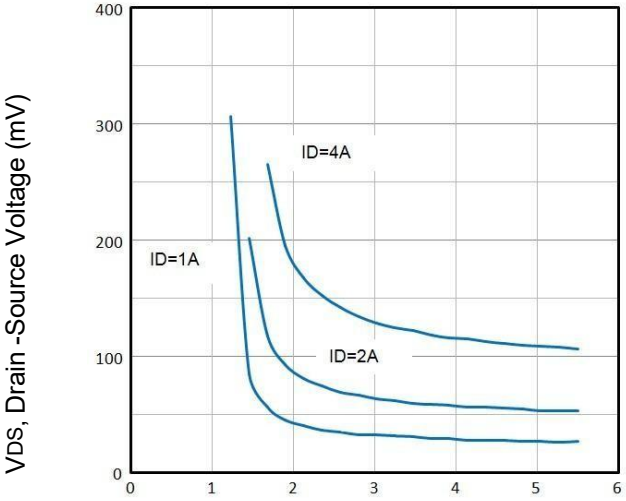


Fig4. Drain -Source Voltage vs Gate -Source Voltage

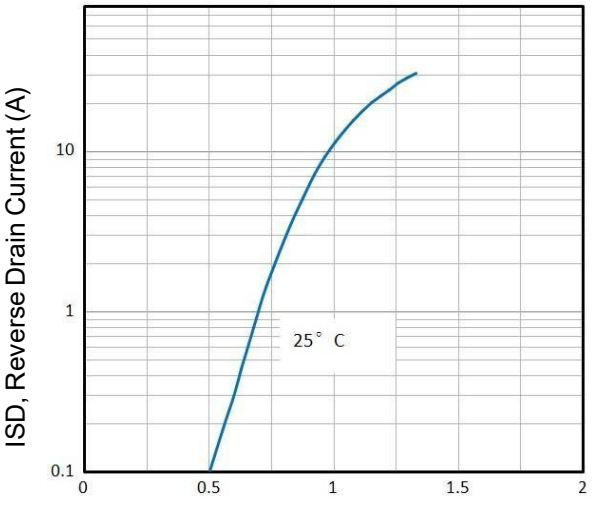


Fig5. Typical Source-Drain Diode Forward Voltage

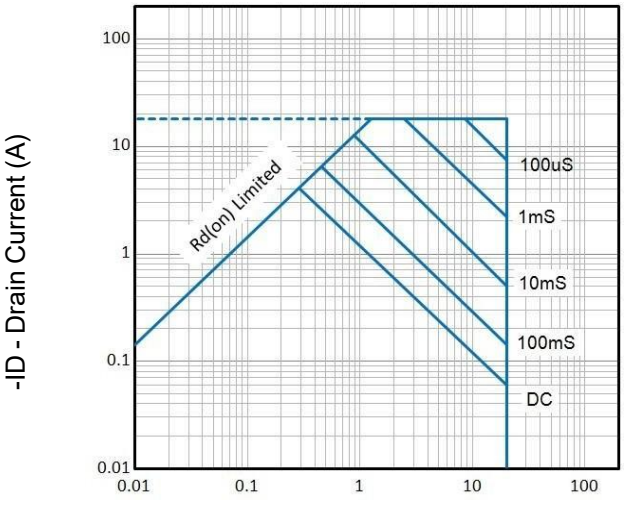


Fig6. Maximum Safe Operating Area

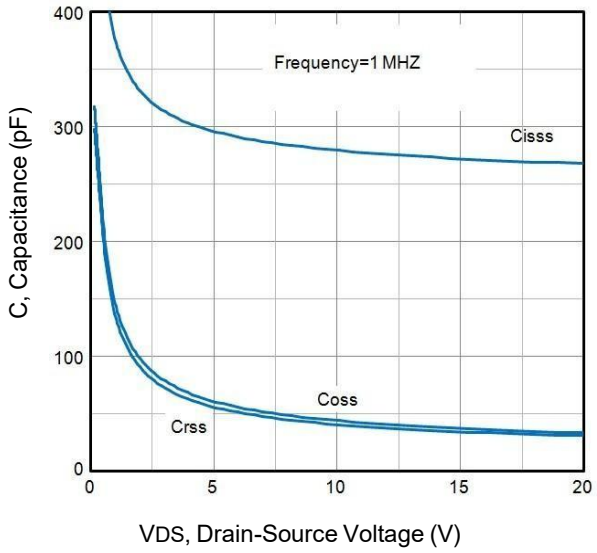


Fig7. Typical Capacitance Vs. Drain-Source Voltage

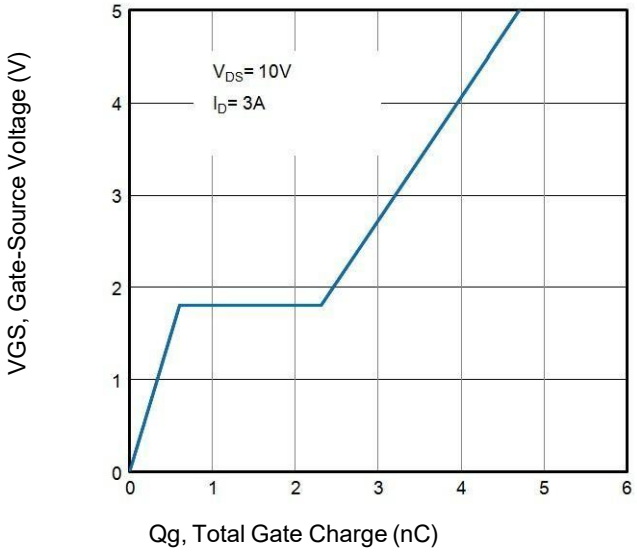


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

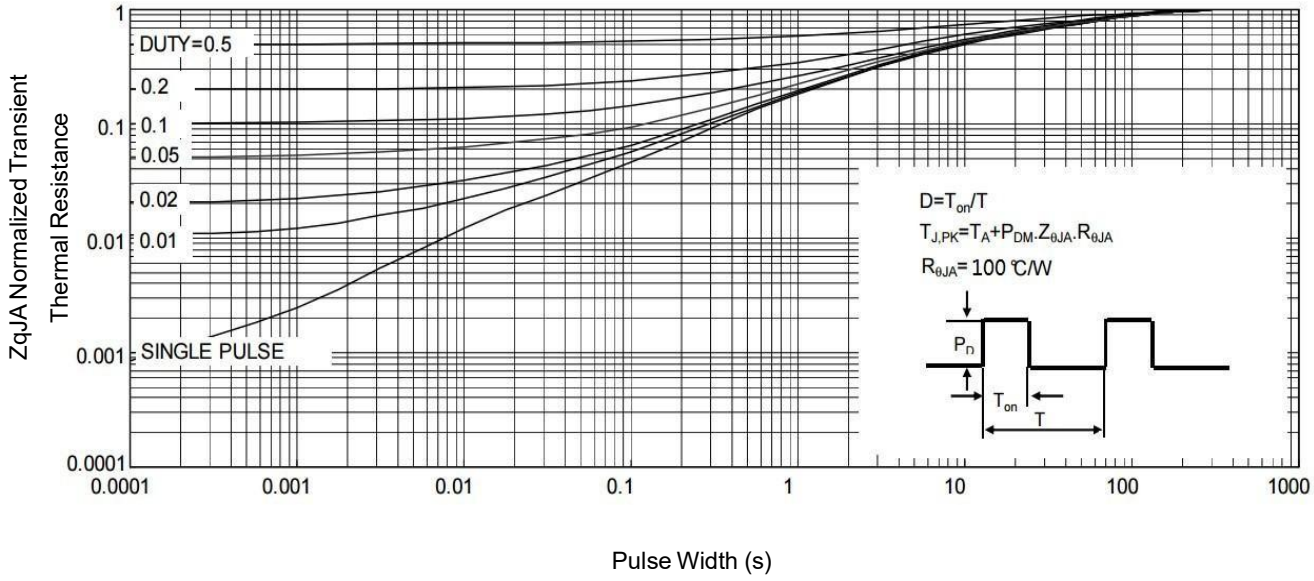


Fig9. Normalized Maximum Transient Thermal Impedance

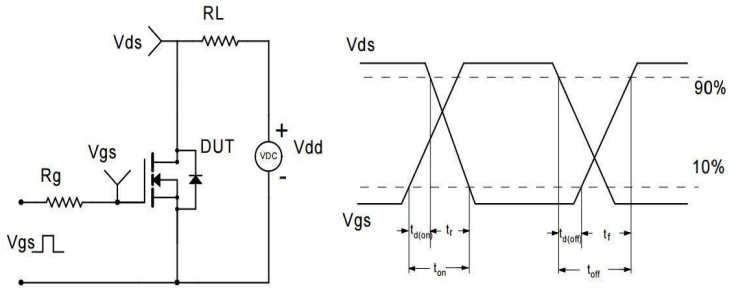
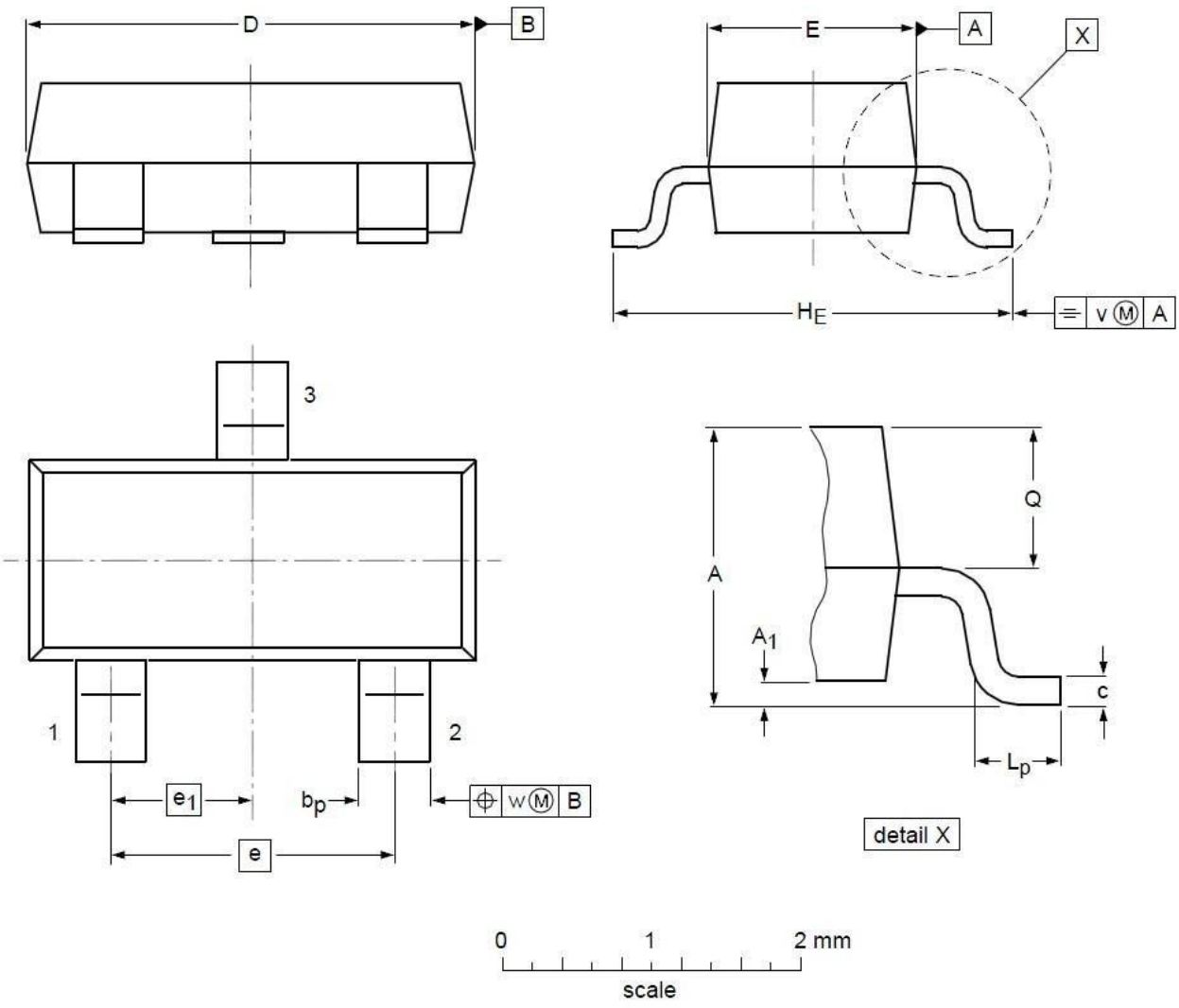


Fig10. Switching Time Test Circuit and waveforms

SOT23 Mechanical Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	**A_1**	0.01	0.05	0.10
b_p	0.30	0.42	0.50	**c**	0.08	0.13	0.15
D	2.80	2.92	3.00	**E**	1.20	1.33	1.40
e	--	1.90	--	**e_1**	--	0.95	--
H_E	2.25	2.40	2.55	**L_p**	0.30	0.42	0.50
Q	0.45	0.49	0.55	**v**	--	0.20	--
w	--	0.10	--				