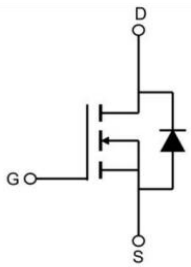
AP30H180K

N-Channel Enhancement Mosfet

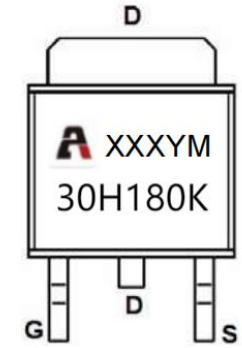
DATA SHEET

Features

- 30V,180A
  $R_{DS(ON)}$ <2.4mΩ @V_{GS}=10V TYP: 2.1mΩ
  $R_{DS(ON)}$ <3.5mΩ @V_{GS}=4.5V TYP: 2.8mΩ
- Advanced Trench Technology
- Lead free product is acquired
- Excellent $R_{DS(ON)}$ and Low Gate Charge



Schematic Diagram



Marking and pin assignment

Applications

- PWM applications
- Load Switch
- Power management

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
30H180K	AP30H180K	TO-252	-	-	2500

ABSOLUTE MAXIMUM RATINGS (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current (Tc=25℃) (1)	I_D	180	A
Continuous Drain Current (Tc=100℃)	I_D	117	A
Pulsed Drain Current (1)	I_{DM}	720	A
Drain Power Dissipation	P_D	96	W
Single Pulsed Avalanche Energy (2)	E_{AS}	484	mJ
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	35	℃/W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.3	℃/W
Junction Temperature	T_J	-55~ +150	℃
Storage Temperature	T_{STG}	-55~ +150	℃

Notes:

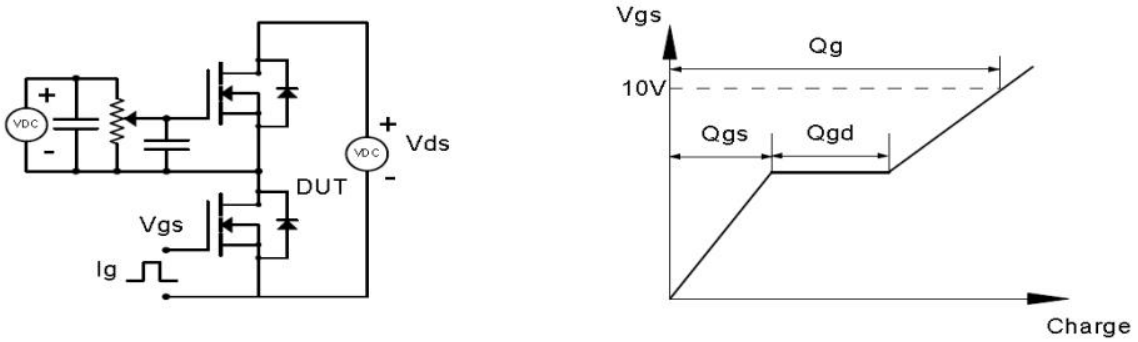
1) Repetitive Rating: pulse width limited by maximum junction temperature
2) EAS condition : T_J=25℃, V_{DD}=15V, V_G=10V, L=0.5mH, R_g=25Ω, I_{AS}=44A

MOSFET ELECTRICAL CHARACTERISTICS(T_J=25℃ unless otherwise noted)

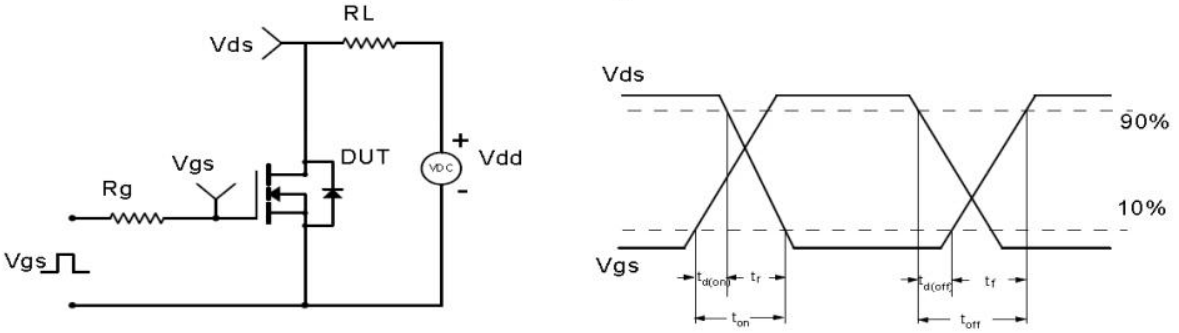
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =250μA	30	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS} =30V, V_{GS} = 0V	-	-	1	μA
Gate-body leakage current	I_{GSS}	V_{GS} =±20V, V_{DS} = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =250μA	1.0	1.4	2.5	V
Drain-source on-resistance	$R_{DS(on)}$	V_{GS} =10V, I_D =40A	-	2.1	2.4	mΩ
		V_{GS} =4.5V, I_D =40A	-	2.8	3.5	
Dynamic characteristics						
Input Capacitance	C_{iss}	V_{DS} =15V, VGS=0V, f=1MHz	-	5060	-	pF
Output Capacitance	C_{oss}		-	575	-	
Reverse Transfer Capacitance	C_{rss}		-	470	-	
Switching characteristics						
Turn-on delay time	$t_{d(on)}$	V_{DD}=15V, I_D=30A, V_{GS}=10V	-	16	-	ns
Turn-on rise time	t_r		-	30	-	
Turn-off delay time	$t_{d(off)}$		-	54	-	
Turn-off fall time	t_f		-	19	-	
Total Gate Charge	Qg	V_{DS}=15V, I_D=30A, R_G = 3Ω, V_{GS}=10V	-	93	-	nC
Gate-Source Charge	Qgs		-	20	-	
Gate-Drain Charge	Qgd		-	23	-	
Source-Drain Diode characteristics						
Diode Forward voltage	V_{SD}	T_J=25℃, V_{GS} =0V, I_S=30A	-	0.8	1.2	V
Diode Forward current	I_S	T_C=25℃	-	-	180	A
Body Diode Reverse Recovery Time	Trr	T_J=25℃, di/dt=100A/us,I_F=30A	-	23	-	ns
Body Diode Reverse Recovery Charge	Qrr		-	14	-	nC

Test Circuit & Waveform

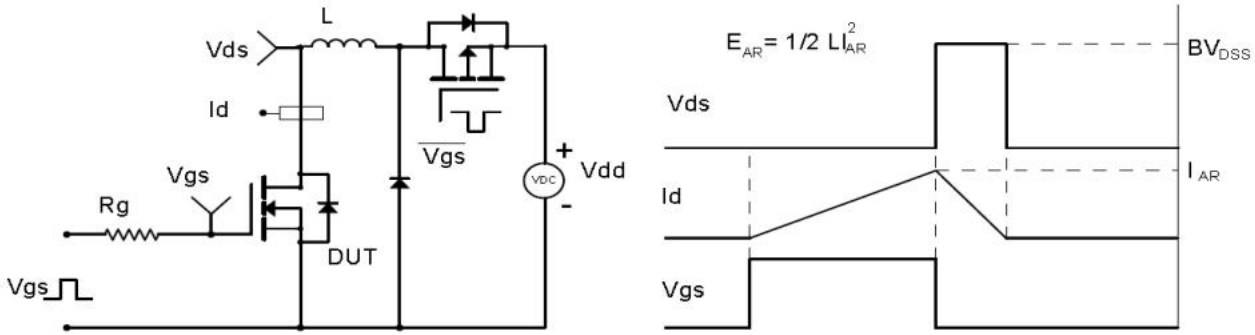
Gate Charge Test Circuit & Waveform



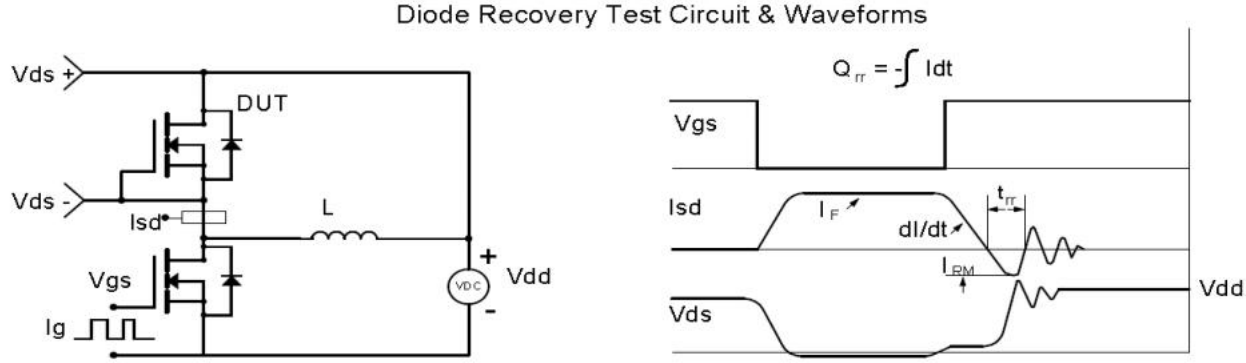
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Typical Characteristics

Figure 1: Output Characteristics

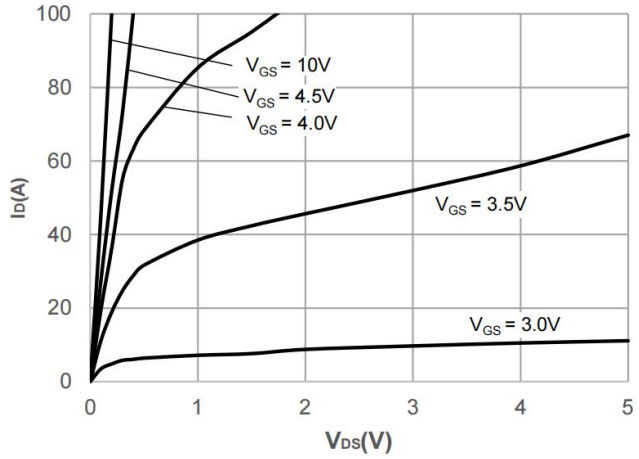


Figure 2: Typical Transfer Characteristics

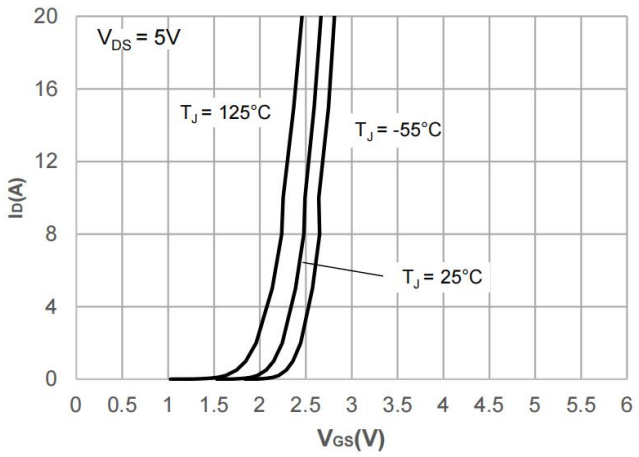


Figure 3: On-resistance vs. Drain Current

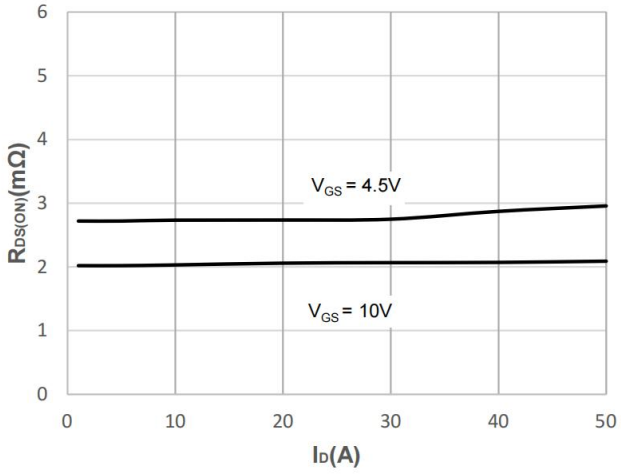


Figure 4: Body Diode Characteristics

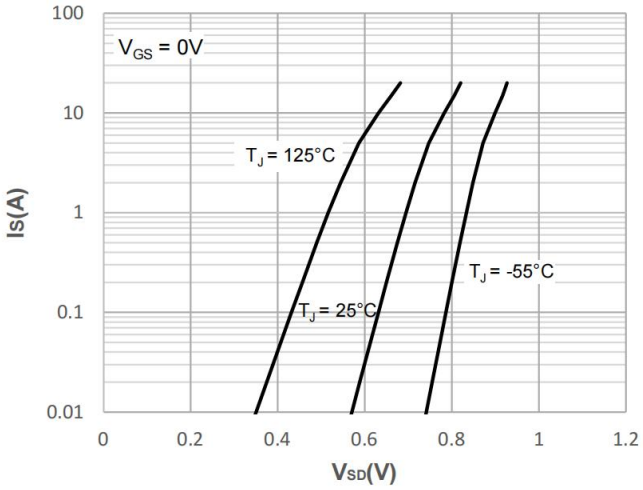


Figure 5: Gate Charge Characteristics

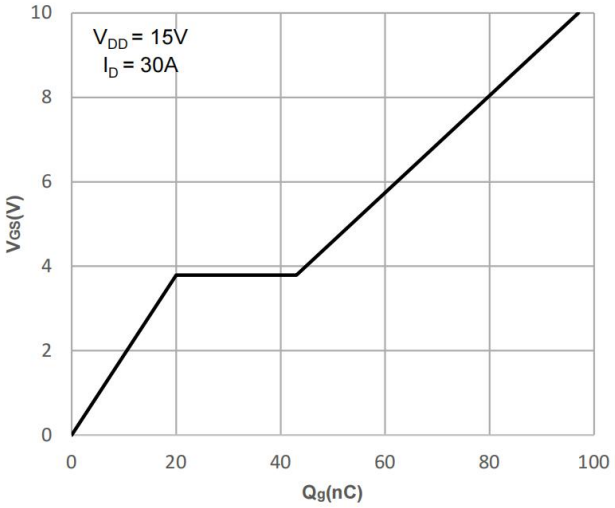
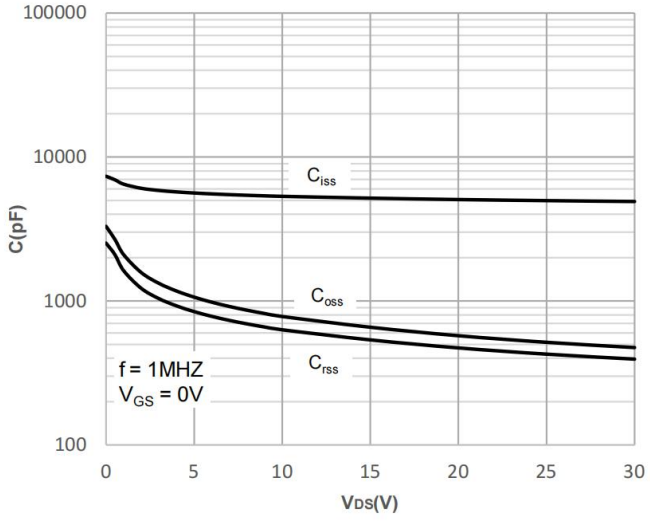


Figure 6: Capacitance Characteristics



Typical Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

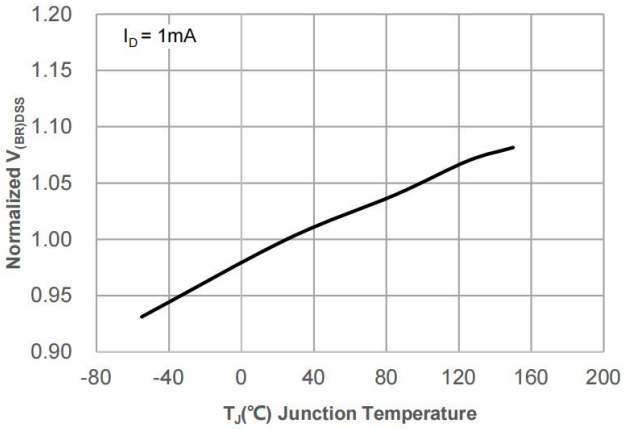


Figure 8: Normalized on Resistance vs. Junction Temperature

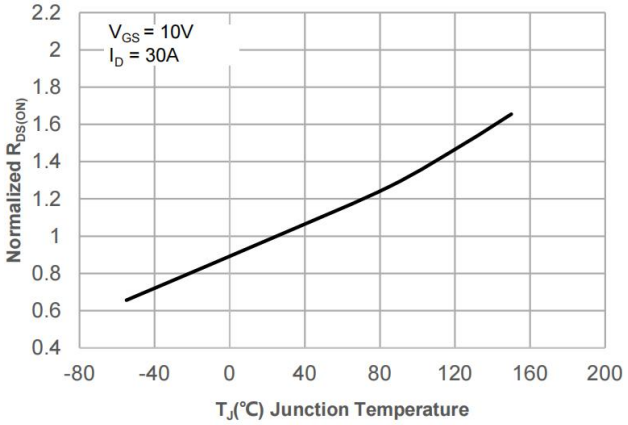


Figure 9: Maximum Continuous Drian Current vs. Case Temperature

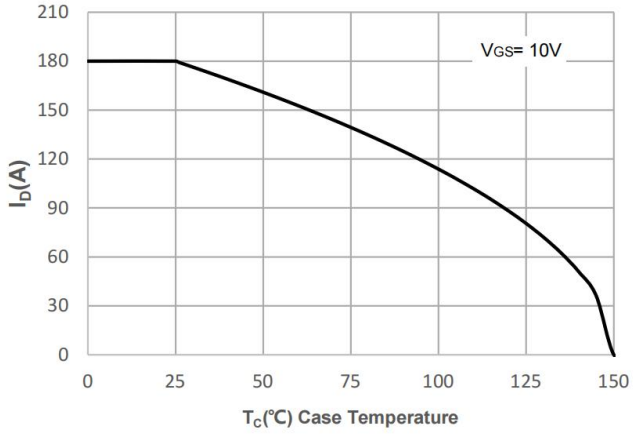


Figure 10: Maximum Safe Operating Area

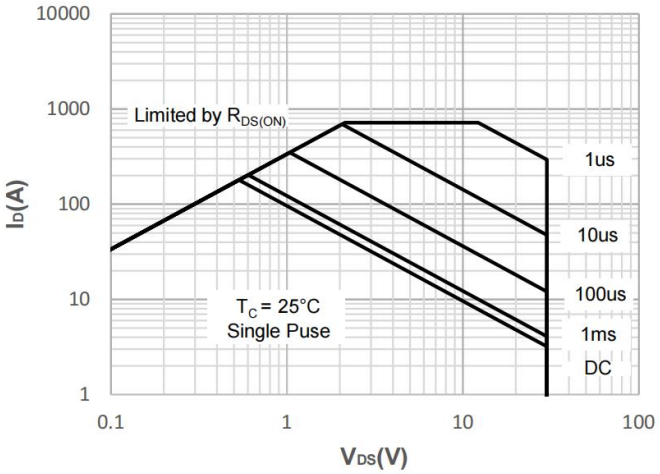
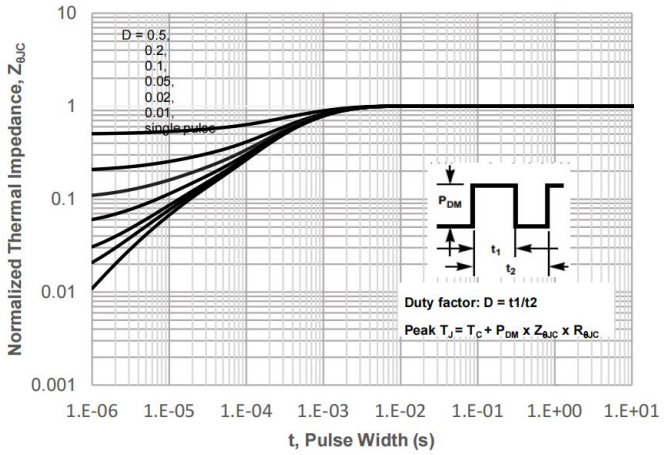
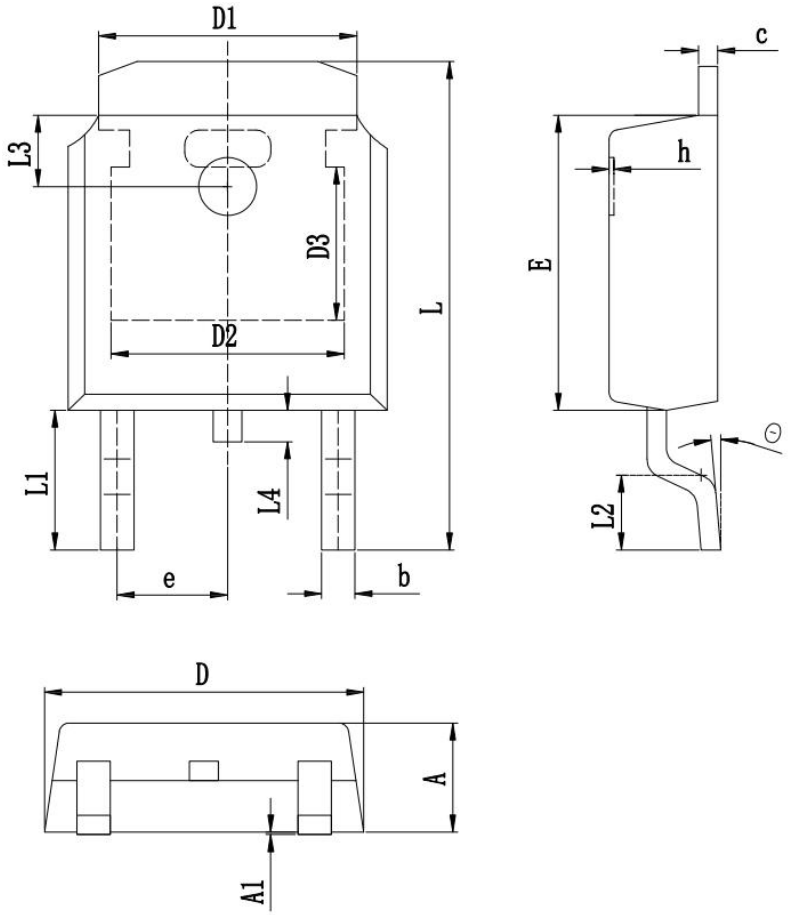


Figure 11: Normalized Maximum Transient Thermal Impedance



TO-252 Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.500	0.690	0.900
c	0.450	0.520	0.580
D	6.450	6.600	6.750
D1	5.100	5.334	5.500
D2	4.700	4.826	5.000
D3	3.000	3.166	3.250
E	5.950	6.100	6.250
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.450	10.100	10.200
L1	2.888 REF		
L2	1.250	1.550	1.750
L3	1.600 REF		
L4	0.600	0.800	1.000
θ	0°		8°

Revision History

Revision	Release	Remark
V1.2	2024/11/11	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product,but similar performance is not guaranteed under other working conditions,and cannot be guaranteed when installed with other products or equipment.To achieve the required performance of the product in actual scenarios,the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings,operating conditions,or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which
requires high reliability.Customers using or selling these products for use in medical,life-saving,or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements,the information described or contained herein may be changed without prior notice.